

Abstracts

A novel integrated microwave bias network for low cost multistage amplifiers (1997 [RFIC])

H. Morkner, M. Frank, K. Negus and Tsong-Ming Kao. "A novel integrated microwave bias network for low cost multistage amplifiers (1997 [RFIC])." 1997 Radio Frequency Integrated Circuits (RFIC) Symposium 97. (1997 [RFIC]): 161-164.

A novel MMIC bias network topology utilizing FETs has been developed. The bias network is very compact, providing low DC path loss with high, broad band RF isolation. This bias network consists of two FETs and a capacitor, and is implemented in pseudomorphic HEMT. The bias network is useful in multi-stage amplifiers where stage-to-stage RF isolation is critical to system stability and performance. A VGA (Variable gain amplifier) is shown with measured results as a application example of the bias network. There is no other known published information or patents on this bias network topology.

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